

## Correction to Toward the Synthesis of Wafer-Scale Single-Crystal Graphene on Copper Foils

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On the third page of the Supporting Information, in the **Graphene transfer to SiO<sub>2</sub>/Si wafers** and **Graphene transfer to TEM grids** sections, the etchant should be (NH<sub>4</sub>)<sub>2</sub>S<sub>2</sub>O<sub>8</sub> rather than (NH<sub>4</sub>)<sub>2</sub>SO<sub>4</sub>. This is due to a writing error, and the change does not affect any parameter and conclusion in this paper.

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